

PROGRESS WHITE PAPERS 2006

**embedded systems design
networks and connected systems
verification and validation
networks on chip**

INHOUDSOPGAVE

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INTRODUCTION

PROGRESS, Program for Research on Embedded Systems and Software, initiates and coordinates Dutch academic research in named areas since 1998. Over the years about 25 projects were maintained in five tenders and over 60 academic researchers collaborated with industrial colleagues on a wide spectrum of embedded subjects. Through its set-up and its results PROGRESS has become a landmark on demand-driven academic research in The Netherlands.

Instead of issuing a sixth tender, PROGRESS management has decided to spend their remaining efforts and money on optimizing the impact of the projects for the Dutch economy. In this framework we coined the term UVO, for Utilization, Valorization and Outreach and issued a Call for Proposals for UVO-projects.

In order to inform our industrial and academic partners about the state of the art in PROGRESS, we organized four symposia on the following four embedded themes:

- Embedded systems design - on May 10, 2006
- Networks and connected systems - on May 11, 2006
- Verification and validation of embedded systems - on May 31, 2006
- Networks on chip - on June 1, 2006

We asked four keynote speakers, all exponents of the Dutch academia, to write white papers on these themes, containing the general state of the art in the four fields and the contributions of PROGRESS projects belonging to these themes more specifically.

This booklet bundles these white papers and hence forms a good survey of the present state of the Dutch academic embedded systems research.

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COLOFON

PROGRESS White Papers 2006

Embedded Systems Design; Networks and Connected Systems;
Verification and Validation; Networks on Chip

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